L Number	Hits	Search Text	DB	Time stamp
1	59	map\$4 with (cache\$1 with (memory adj line))	USPAT;	2004/09/17 13:17
			US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
2	72	(cache\$1 with (memory adj line)) with (location\$ or postion\$1)	USPAT;	2004/09/17 13:19
i		, , , , , , , , , , , , , , , , , , , ,	US-PGPUB;	
]			EPO; JPO;	
			DERWENT;	
l, (215	(711 (120) CCI C	IBM_TDB	2004/09/17 13:19
3	315	(711/129).CCLS.	USPAT; US-PGPUB;	2004/09/17 13:19
			EPO; JPO;	1
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{			IBM_TDB	
4	2	,	USPAT;	2004/09/17 13:23
		((711/129).CCLS.)	US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM_TDB	
5	25	((determin\$5 or assign\$3 or defin\$3 or decid\$3) near10 (position\$1 or	USPAT;	2004/09/17 13:25
,		location\$1) near10 (memory adj line))	US-PGPUB;	2001/0//2/ 25:25
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
6	11	map\$4 near10 ((line\$1 or slice\$1 or section\$1) near2 memory) with (cache near3	USPAT;	2004/09/17 13:29
		(section\$1 or partition\$1))	US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM_TDB	
7	61	(determin\$5 or assign\$3 or defin\$3 or decid\$3 or allocat\$3) near10 ((line\$1 or	USPAT:	2004/09/17 13:30
		slice\$1 or section\$1) near2 memory) with (cache near3 (section\$1 or	US-PGPUB;	
		partition\$1))	ЕРО; ЈРО;	i
			DERWENT;	
8	64	/gggard\$2 or hase\$1) with ((logation\$1 or postion\$1) pour\$ ((line or align or	IBM_TDB	3004,00,1713 33
•	04	(accord\$3 or base\$1) with ((location\$1 or postion\$1) near5 ((line or slice or section) near2 memory))	USPAT; US-PGPUB;	2004/09/17 13: 32
		Socion, near memory,	EPO; JPO;	
			DERWENT:	
			IBM_TDB	!
9	11	((711/129). CCLS.) and ((determin\$5 or assign\$3 or defin\$3 or decid\$3 or	USPAT;	2004/09/17 13: 33
		allocat\$3) near10 ((line\$1 or slice\$1 or section\$1) near2 memory) with (cache	US-PGPUB;	
		near3 (section\$1 or partition\$1)))	EPO; JPO;	
			DERWENT; IBM_TDB	
11	1	((711/129).CCLS.) and ((accord\$3 or base\$1) with ((location\$1 or postion\$1)	USPAT:	2004/09/17 13: 33
! I		near5 ((line or slice or section) near2 memory)))	US-PGPUB;	
			EPO; JPO;	
ĺ			DERWENT,	
	115	(711 (120) CCT C	IBM_TDB	
_	315	(711/129).CCLS.	USPAT;	2004/09/14 11:16
{			US-PGPUB; EPO; JPO;	
1			DERWENT;	
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-]	285	((711/129).CCLS.) and @ad<=20011109	USPAT;	2004/09/14 14:48
I			US-PGPUB;	
}			EPO; JPO;	
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_ {	233	((711/129).CCLS.) and (cach\$3 near10 (location\$1 or place\$1 or address\$2))	IBM_TDB USPAT:	2004/00/14 11 10
I	200	(1) and and the following from the first of place of address 22))	US-PGPUB:	2004/09/14 11:18
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-	216	(((711/129).CCLS.) and (cach\$3 near10 (location\$1 or place\$1 or address\$2))) and	USPAT;	2004/09/14 11:18
ĺ		@ad<=20011109	US-PGPUB;	
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_	7	((711/129).CCLS.) and (cach\$3 near10 (location\$1 or place\$1 or address\$2)) and	USPAT;	2004/09/14 14: 47
ļ		(allocat\$3 near\$ policy)	US-PGPUB;	2007/07/17 14:4/
			EPO; JPO;	
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-	7	((711/129).CCLS.) and (cach\$3 near10 (location\$1 or place\$1 or address\$2)) and (allocat\$3 near5 (policy or rule\$1))	USPAT; US-PGPUB;	2004/09/14 11: 32
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-	212	allocation adj (polity or rule)	IBM_TDB USPAT;	2004/09/14 11: 33
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-	77	(allocation near5 (policy or rule)) same cach\$3	USPAT;	2004/09/14 13: 22
			US-PGPUB; EPO; JPO;	
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-	14	(allocat\$3 near5 based near5 location\$1) same cach\$3	USPAT;	2004/09/14 13: 34
	}		US-PGPUB; EPO; JPO;	
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1_	606	/mamony near largetion 11 near 10 determines more 10 angle 2	IBM_TDB	2004/00/14 12 25
] -	000	(memory near5 location\$1) near10 determin\$5 near10 cach\$3	USPAT; US-PGPUB;	2004/09/14 13: 35
	İ		EPO; JPO;	
}			DERWENT;	
_	18	((711/129).CCLS.) and ((memory near5 location\$1) near10 determin\$5 near10	IBM_TDB USPAT;	2004/09/14 14: 02
		cach\$3)	US-PGPUB;	2004/0//14 14:02
}			EPO; JPO;	
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-	346	(address\$2 near3 memory) near10 based near10 cach\$3	USPAT;	2004/09/14 14: 05
		•	US-PGPUB;	
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-	12	((711/129).CCLS.) and ((address\$2 near3 memory) near10 based near10 cach\$3)	USPAT;	2004/09/14 14:05
<u> </u>			US-PGPUB; EPO; JPO;	
			DERWENT;	
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-	24	dynamic\$4 near5 (adjust\$3 or vary\$3) near5 size near5 cache	USPAT; US-PGPUB;	2004/09/14 14:44
			EPO; JPO;	
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]_	192	((711/129).CCLS.) and ((based or determin\$3) near10 (location\$2 or address\$2))	IBM_TDB USPAT;	2004/09/14 14: 58
	• • • • • • • • • • • • • • • • • • •	Waster 1.00130.1 and Housed of deterministy fically (locations 2 of address \$2))	US-PGPUB;	2004/07/14 14: 38
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-	177	··· ·· ···	USPAT;	2004/09/14 14:48
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] -	749	(711/133).CCLS.	USPAT;	2004/09/14 15: 14
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-	339	(711/153).CCLS.	USPAT; US-PGPUB;	2004/09/14 15: 15
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1-	386	(711/159).CCLS.	USPAT:	2004/09/14 15: 15
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-	703	(711/173).CCLS.	USPAT;	2004/09/14 15:15
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File Cabinet	Partitioning regular grid applications with irregular boundaries for cache-coherent multiprocessors Yang Zeng; Abraham, S.G.; Parallel Processing Symposium, 1995. Proceedings., 9th International, 25-28 1995 Pages: 222 - 228		
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4 An argument for simple COMA

Saulsbury, A.; Wilkinson, T.; Carter, J.; Landin, A.; High-Performance Computer Architecture, 1995. Proceedings. First IEEE Symposium on , 22-25 Jan. 1995 Pages: 276 - 285

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5 Reducing branch delay to zero in pipelined processors

Gonzalez, A.M.; Llaberia, J.M.;

Computers, IEEE Transactions on , Volume: 42 , Issue: 3 , March 1993

Pages:363 - 371

[Abstract] [PDF Full-Text (760 KB)] IEEE JNL

6 Reducing power consumption for high-associativity data caches in embedded processors

Nicolaescu, D.; Veidenbaum, A.; Nicolau, A.;

Design, Automation and Test in Europe Conference and Exhibition, 2003 , 201 Pages: 1064 - 1068

[Abstract] [PDF Full-Text (KB)] IEEE CNF

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